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| Application Number: | 10541881 | | | | |
| International Application Number: | | | | | |
| Confirmation Number: | 6245 | | | | |
| Title of Invention: | CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING ELECTRONIC COMPONENTS AGAINST ILLICIT MANIPULATION | | | | |
| First Named Inventor/Applicant Name: | Wolfgang Stidl | | | | |
| Customer Number: | 65913 | | | | |
| Filer: | Jueren Krausepolstorff/Vilimaina Naga | | | | |
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